

Brajesh Kumar Gupta

Contact No: 7209616869

E-mail : bkgupta8bu@gmail.com / brajesh8bu@gmail.com

Designation : Asst. professor

Department : Electronics and communication engineering



RESEARCH PAPERS & SEMINAR

- Novel hamming code for Error correction and detection of higher data bit using vhdl
(Published in IJSER Journal)
- Area efficient and fault tolerant FIR Filter
(Published in IJSER Journal)
- Seminar & Training Program in Embedded System (8051 Microcontroller)

AREA OF INTEREST

- VLSI Design
- Communication System
- Digital Signal Processing ,
- Electromagnetic Field Theory
- Microwave engineering

ACADEMIC DETAIL

Master in Electronics and Communication Engineering

Specialization: VLSI Design & Embedded Systems

National Institute Of Technology, Jamshedpur

Bachelor in Electronics and Communication Engineering

University institute of technology (Burdwan university), Burdwan (W.B)

INTERESTED SOFTWARES

- Pspice,
- MATLAB
- Xilinx
- HFSS
- Microwind
- Tanner Tools
- Cadence

ACADEMIC PROJECTS

Bachelor of Engineering Project

Designing Of Delta Modulator

Under Guidance: Samita Hazra , Assistant professor, Depart. Of ECE , Burdwan University

Master Of Technology Project

A Novel Hamming Code Based Design Of Fault Tolerant And Area Efficient FIR Filter Against Single Event Upset (SEUs)

Under Guidance: Prof. Rashmi Sinha (Associate Professor in Depart. Of ECE) , NIT Jamshedpur

PERSONAL DETAILS

- Date of birth : January 06 1986
- Father's Name : Mr. Gopal Prasad Gupta
- Languages known : English, Hindi, Bengali, Bhojpuri
- Permanent Address : Brajesh kumar gupta C/O Gopal Prasad Gupta, Vill-Kharahana ,
P.O – Nateyan , dist. – Kaimur (Bihar) , 821109